

Input/Output Subsystem

Fig. 6 EXAMPLE PERIPHERAL CONTROLLER BLOCK DIAGRAM

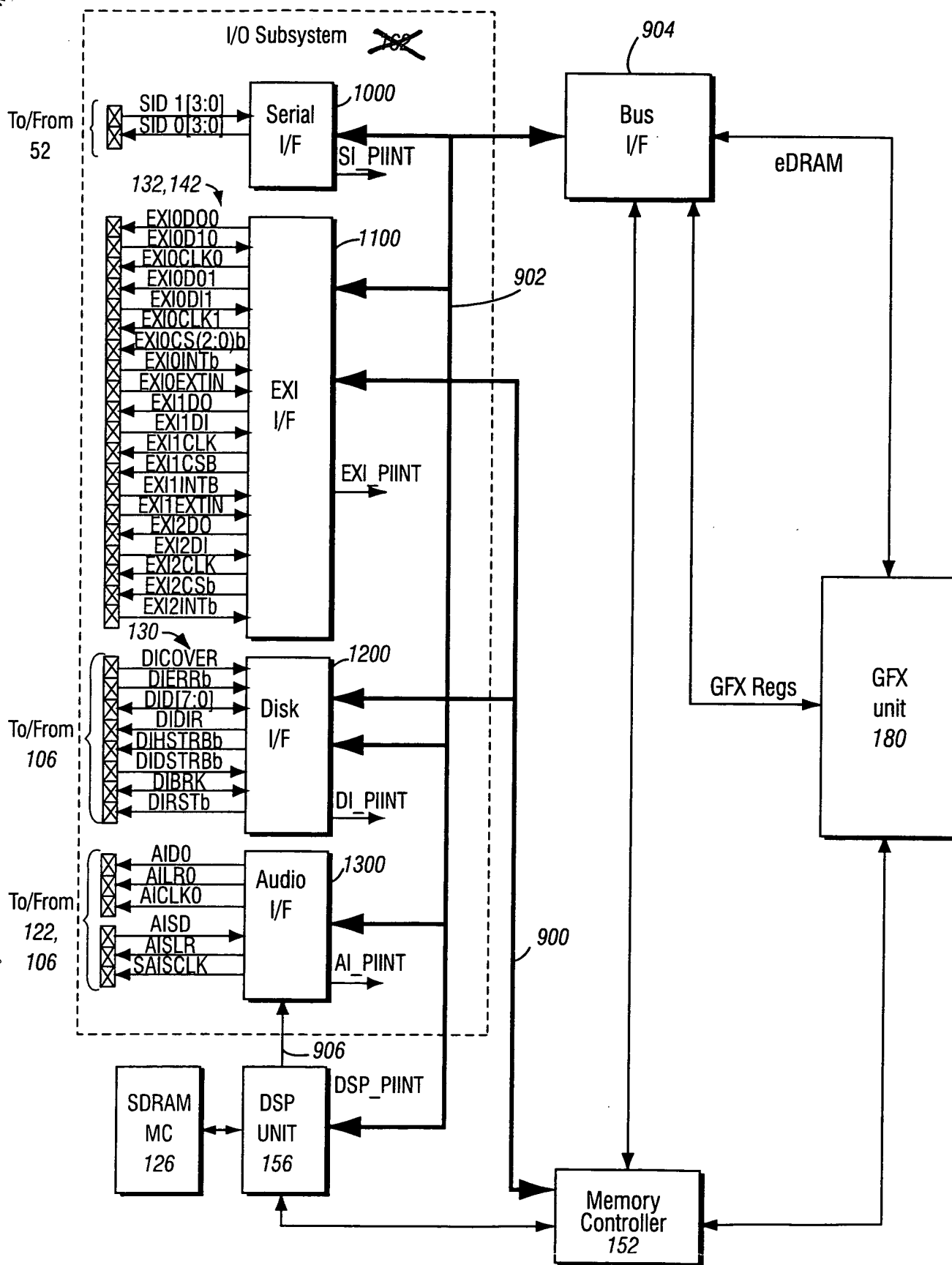


Fig. 3

